



The diagram illustrates a system architecture involving a car (12) and a computer (38). The car contains an Antenna (24) on top, an Antenna Base (20) below it, a RADIO (14) in the center, SPEAKERS (16) on the right, and a MICROPHONE (32) on the far right. A dashed line (30) connects the Antenna to a point (•). An Amplifier (29) is connected to the Antenna Base (20) and the RADIO (14). A line (23) connects the Amplifier to the Antenna Base. A line (34) connects the MICROPHONE to the computer. The computer (38) contains a Memory (48) and a Microprocessor (46). A line (40) connects the Memory to the Microprocessor. A line (44) connects the Microprocessor to a Display (42). A line (26) connects the RF Generator (28) to the computer. A line (36) connects the Digital Signal Processor (27) to the computer. A line (50) connects the RF Generator to the computer. A line (28) connects the Digital Signal Processor to the RF Generator. A line (27) connects the Digital Signal Processor to the computer. A line (36) connects the computer to the Digital Signal Processor.

**Fig. 1**

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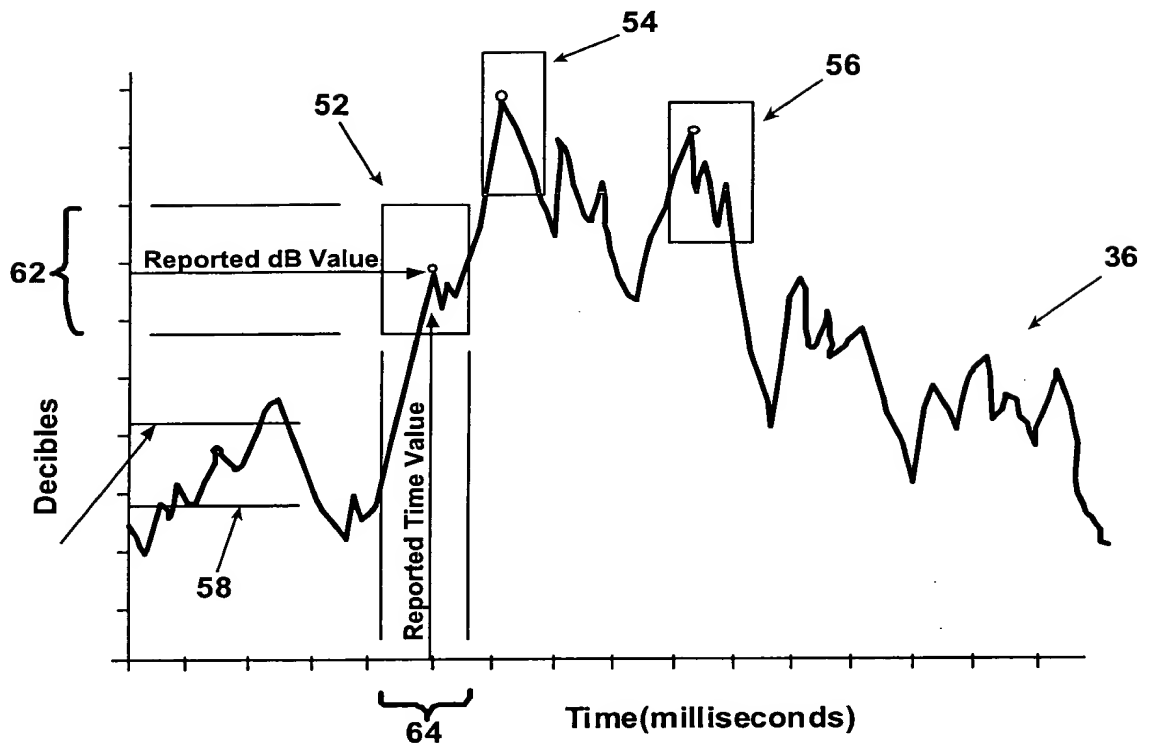
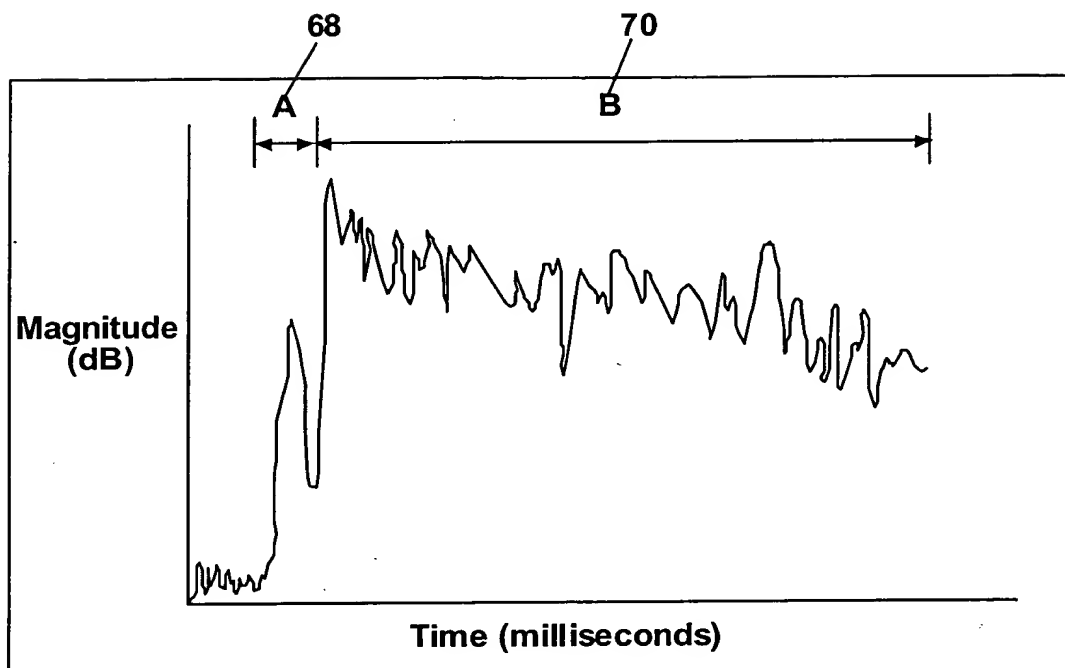
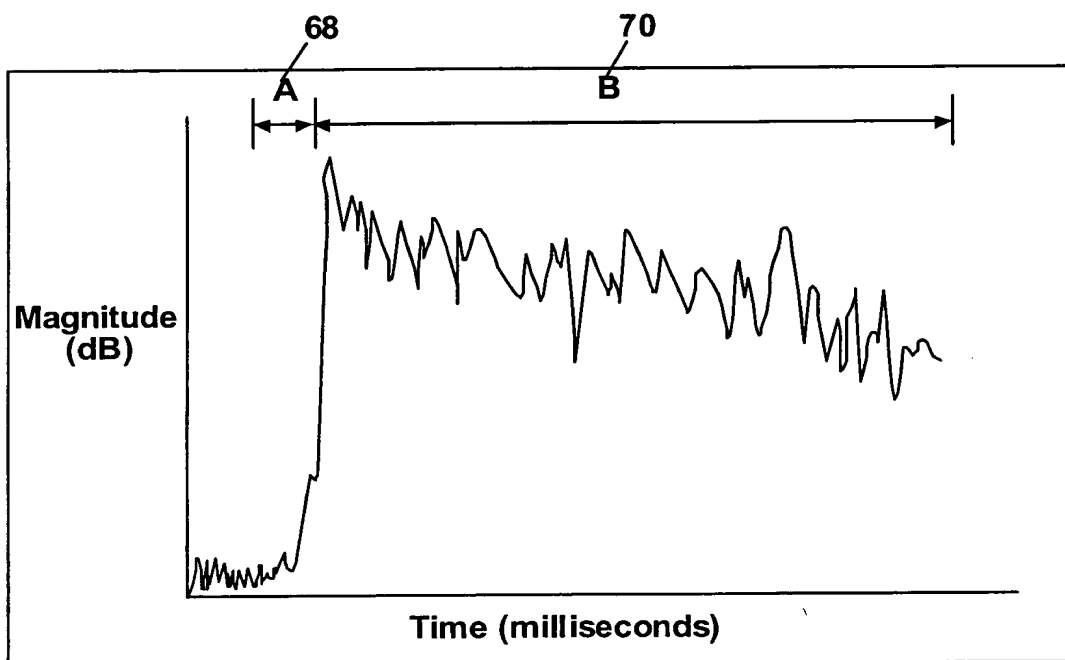


Fig. 2

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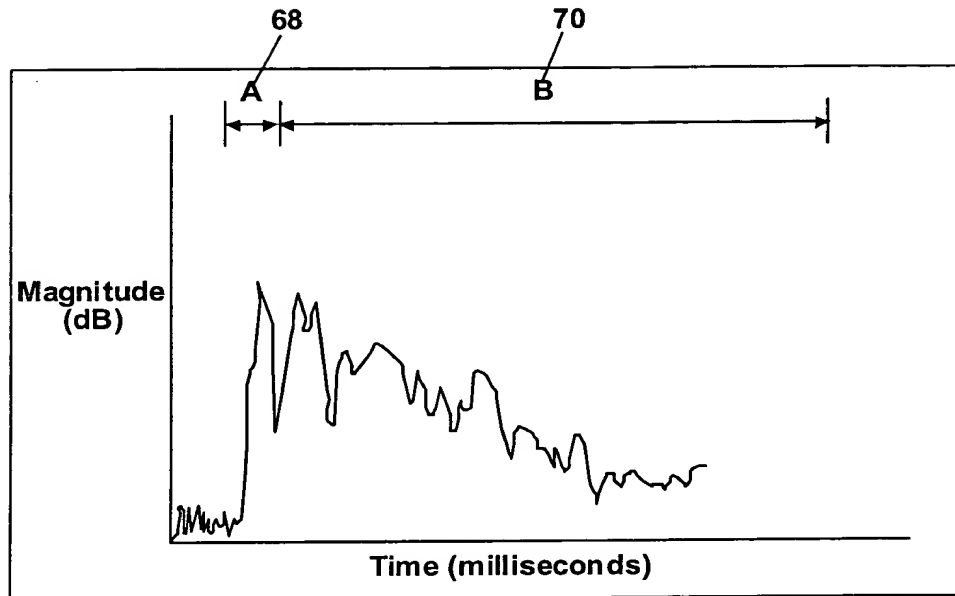


**Fig. 3**

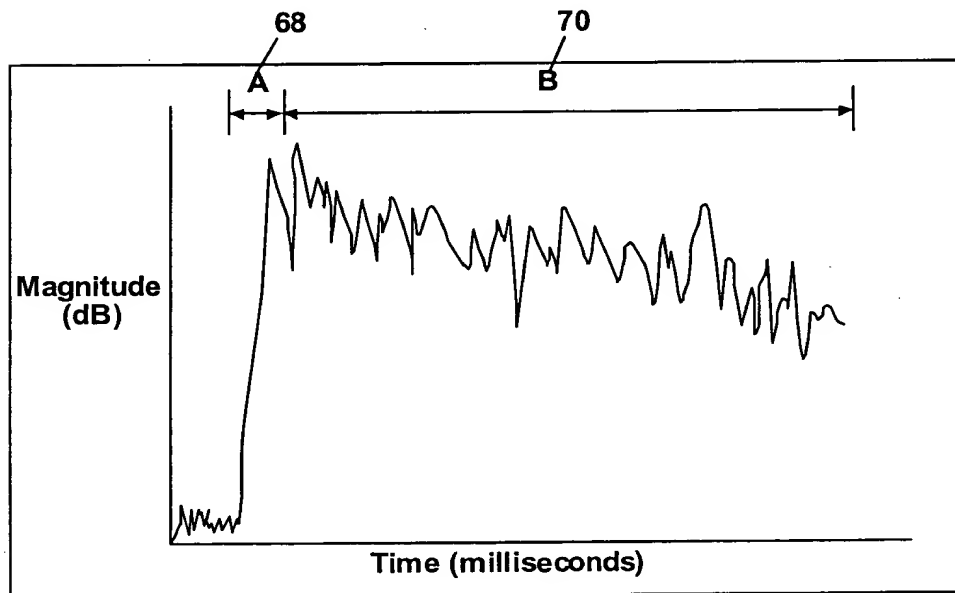


**Fig. 4**

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**Fig. 5**



**Fig. 6**

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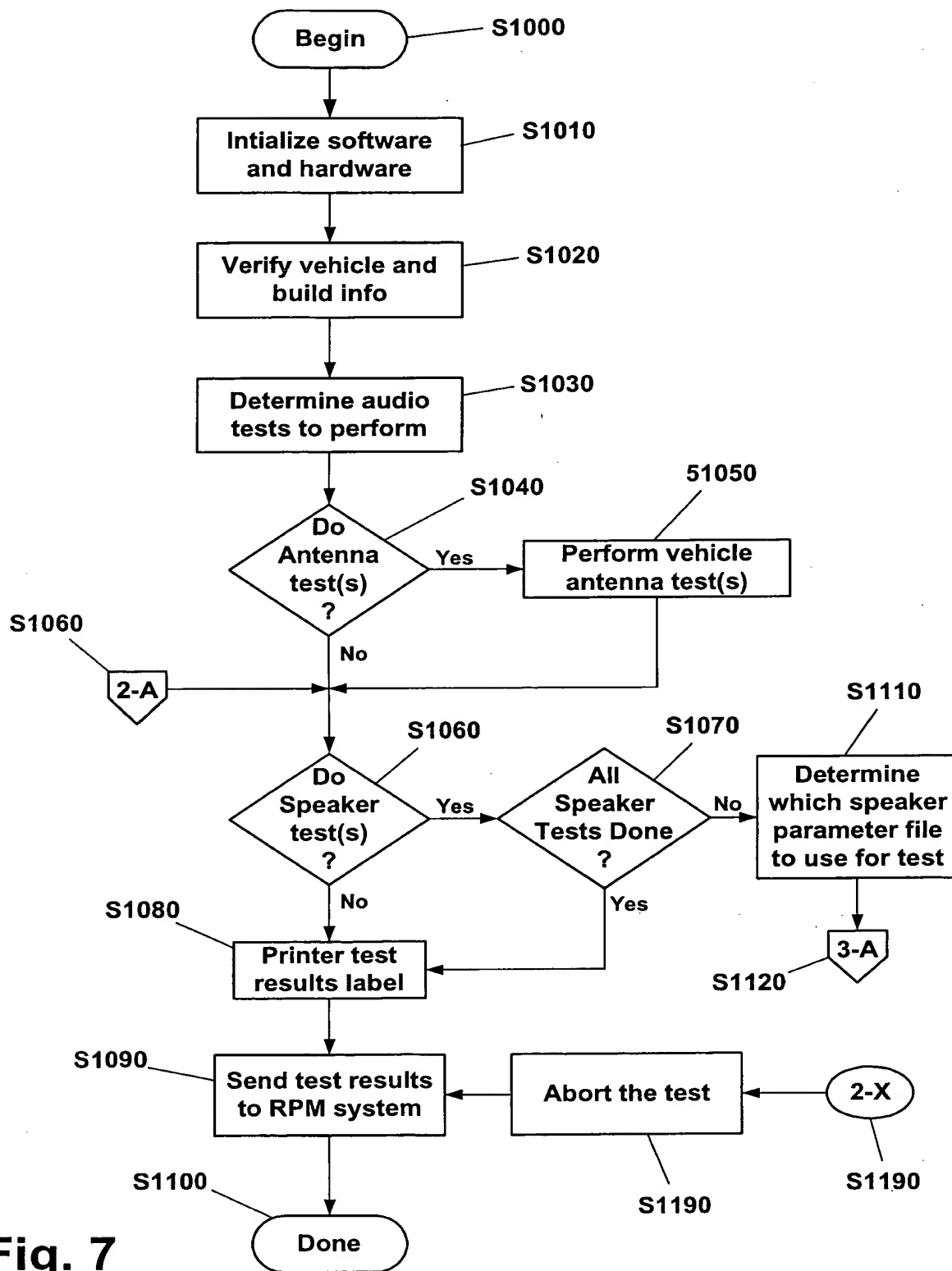
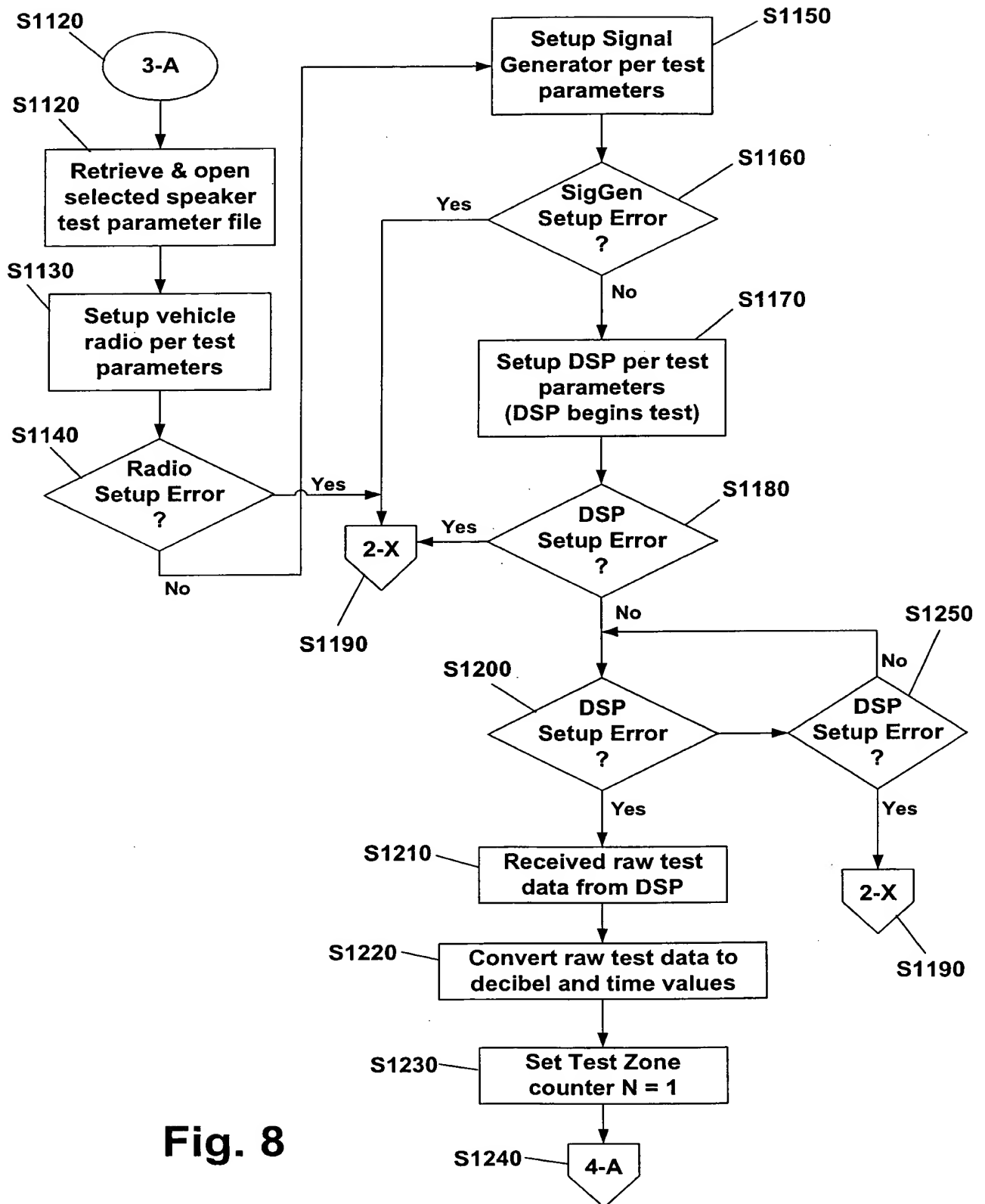


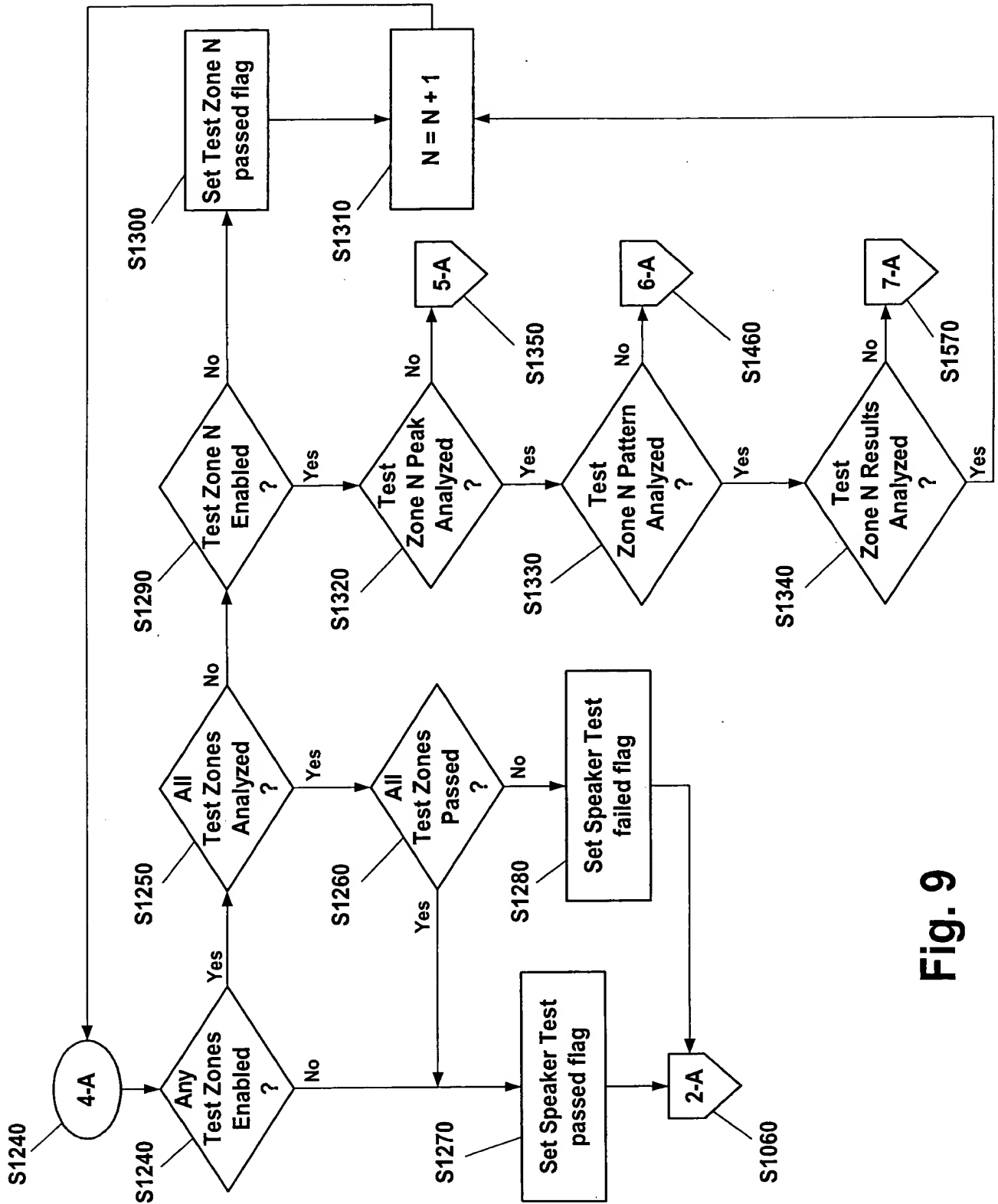
Fig. 7

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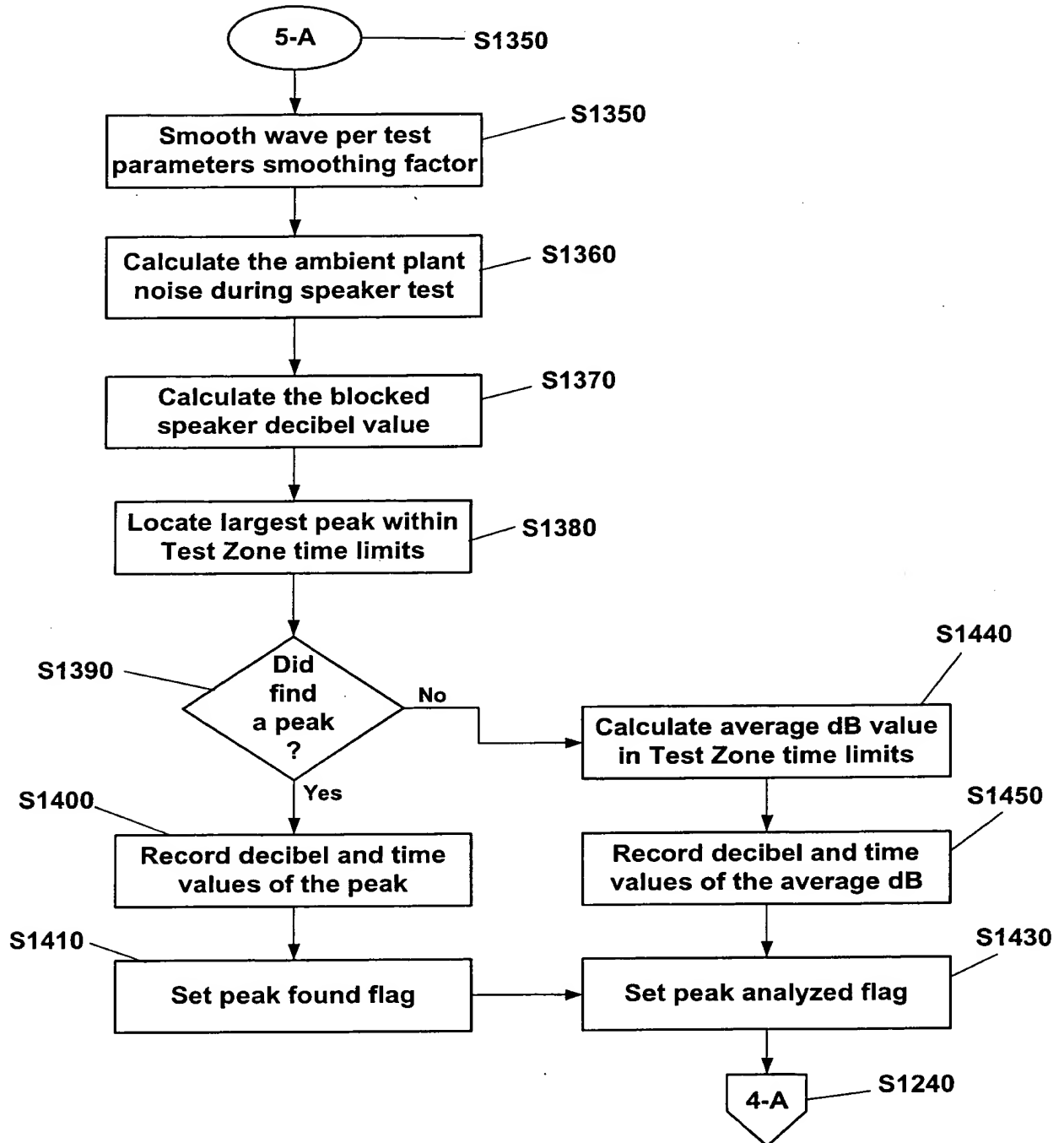
**Fig. 8**

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**Fig. 9**

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**Fig. 10**



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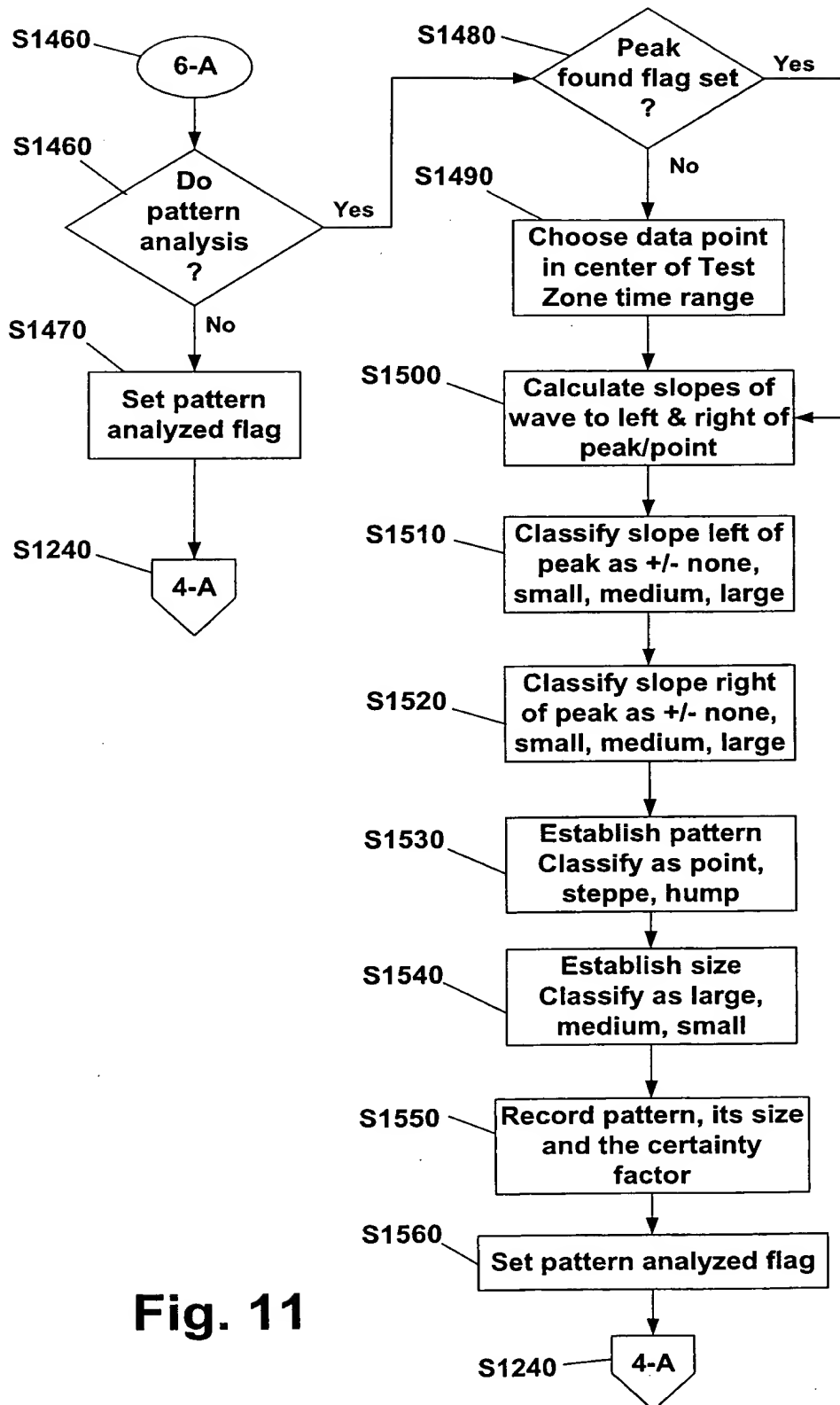
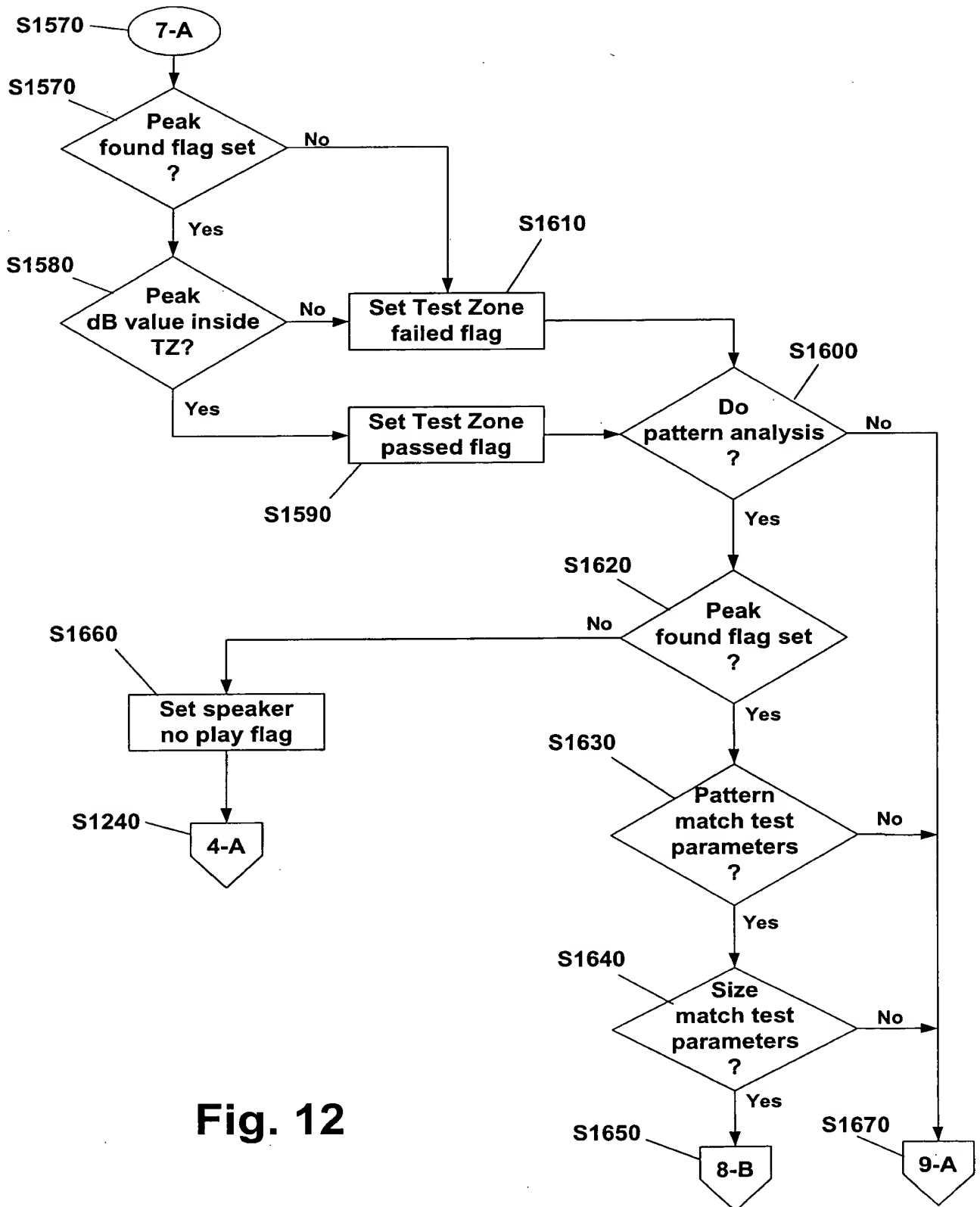


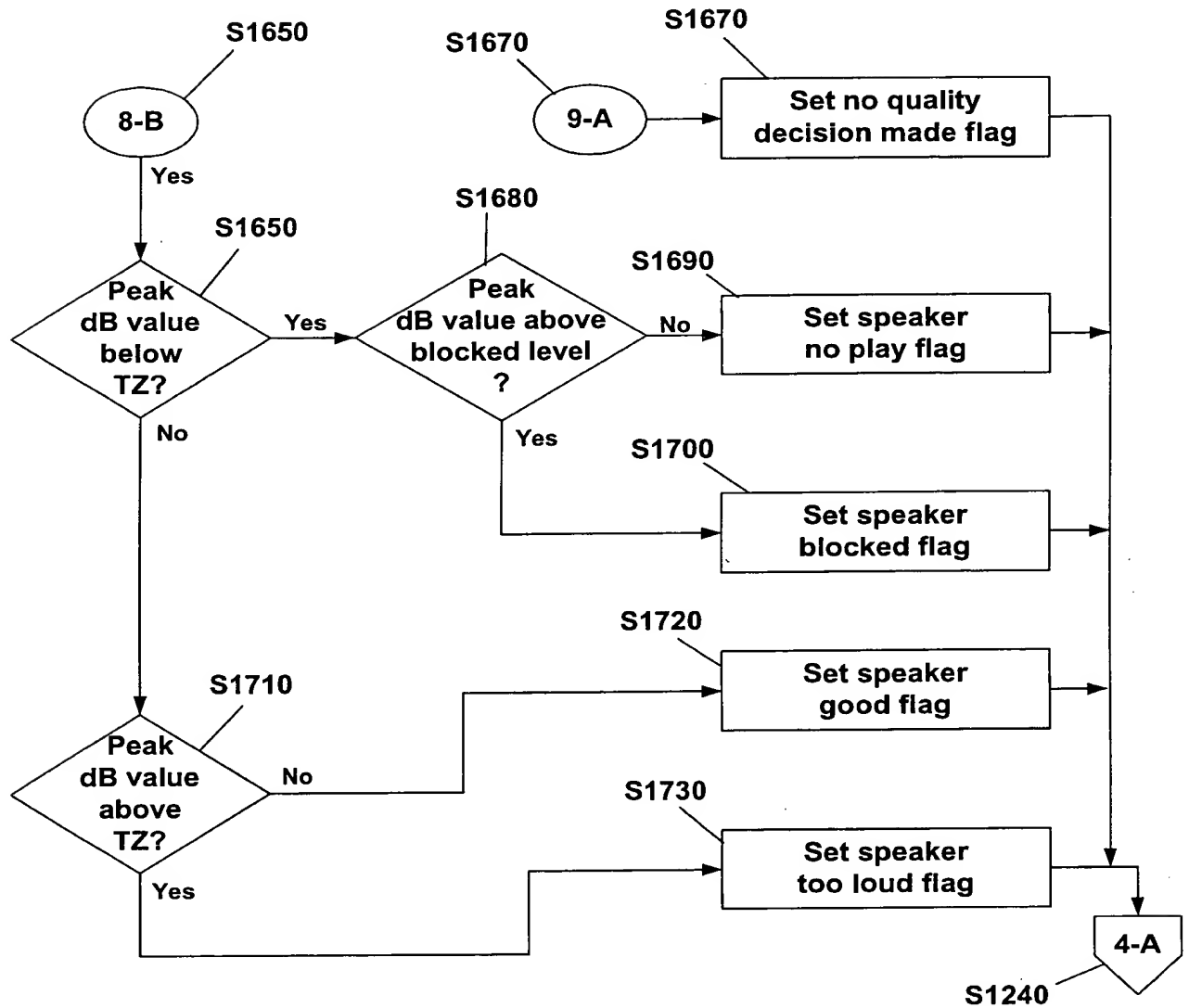
Fig. 11

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**Fig. 12**

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**Fig. 13**